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PTO/SB/08A (08-00)

Approved for use through 10/31/2002. OMB 0651-0031

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Substitute for Form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete If Known	
Sheet		1	of	1	Application Number Filing Date First Named Inventor Group Art Unit Examiner Name Attorney Docket No.
					August 17, 2001 Gerard Chauvel, et al. TI-31360



U.S. PATENT DOCUMENTS						
Exam. Initials*	Cite No.1	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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<input checked="" type="checkbox"/>	CA	Texas Instruments Incorporated, S/N: 09/696,052, filed 10/25/2000, <i>Intelligent Power Management for Distributed Processing Systems</i>	
	CB		
	CC		
	CD		
	CE		
	CF		
	CG		
	CH		

Examiner Signature:	Date Considered: <u>07/07/2004</u>
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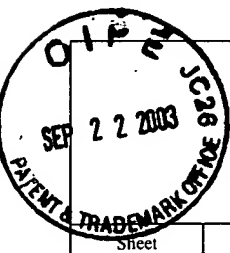
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete If Known

Application Number	
Filing Date	August 17, 2001
First Named Inventor	Gerard Chauvel, et al.
Group Art Unit	2681
Examiner Name	
Attorney Docket No.	TI-31360

U.S. PATENT DOCUMENTS

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		Number	Kind Code ² (if known)			
F.F.	AA	5,339,445 A		Laszlo R. Gasztonyi	8/16/94	<p>RECEIVED</p> <p>SEP 25 2003</p> <p>Technology Center 2600</p>
	AB	5,623,647 A		Amit K. Maitra	4/22/97	
	AC					
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FOREIGN PATENT DOCUMENTS

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F.F.	BA	EP	0 683 451 A		Canon KK	11/22/95		
	BB	EP	0 794 481 A		Mitsubishi Elec. Corp.	9/10/97		
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F.F.	CA	R. Young, et al.; <i>Adaptive Clock Speed Control for Variable Processor Loading</i> , Motorola Technical Developments, Motorola, Inc., Schaumburg, IL, US, Vol. 15, May 1, 1992, pgs. 43-44.	
F.F.	CB	J.A. Manzak, et al.; <i>Variable Voltage Task Scheduling for Minimizing Energy or Minimizing Power</i> , 2000 IEEE Int'l Conf. on Acoustics, Speech & Signal, Vol. 6, June 5, 2000, pgs. 3239-3242.	
	CC		
	CD		
	CE		
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	CH		

Examiner Signature	<i>H3m Henry</i>	Date Considered	7/07/2004
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